

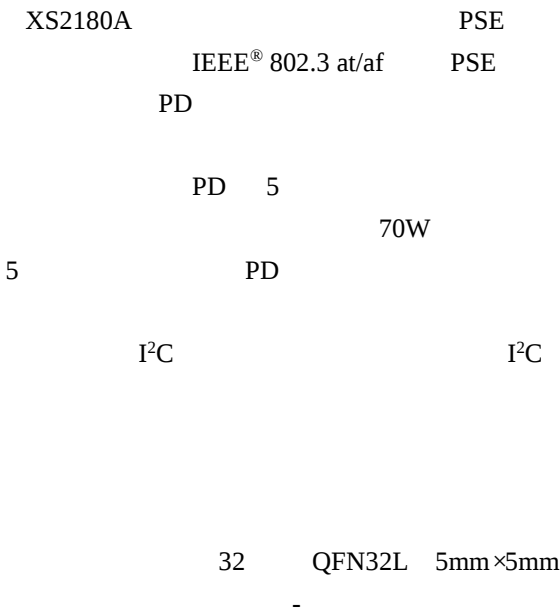
XS2180A

IEEE 802.3 at/af

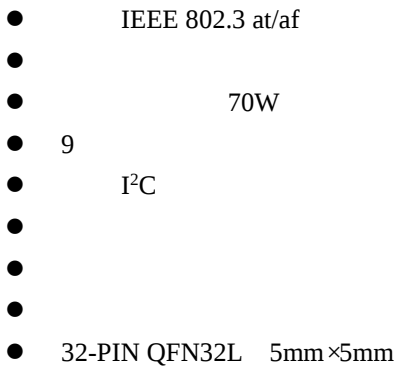
PSE

1

1.1



1.2



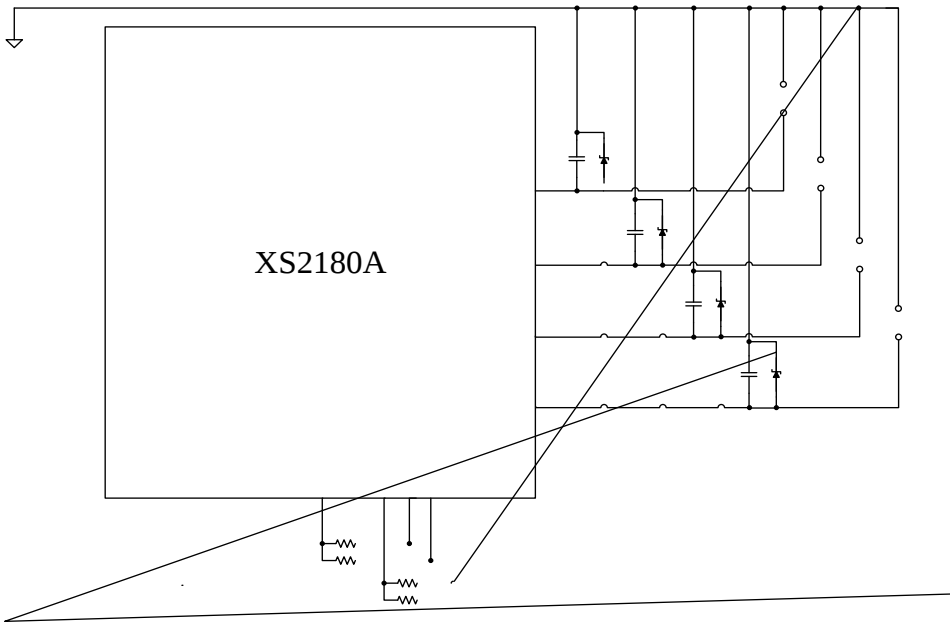
1.3



1.4

PART	TEMP RANGE	PIN-PACKAGE
XS2180A	-40 105	QFNWB5 5-32L

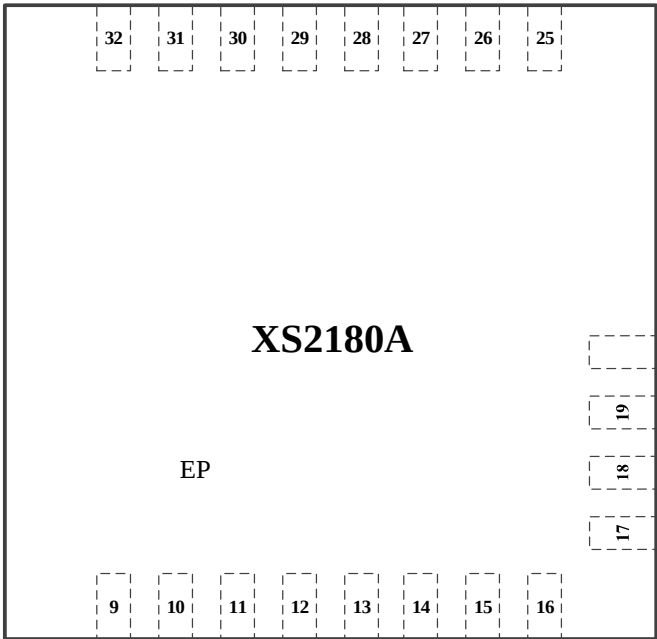
2



3

2024	11	V1.0	

4



SCL	1	I	DGND	I ² C
SDAOUT	2	OD	DGND	I ² C
SDAIN	3	I	DGND	I ² C

			DGND
EN	4	I	DGND V _{DD}
A0, A1, A2, A3	5/6/7/8	IL	I ² C Addr 0, 1, 2, 3 3, 2, 1, 0 0:1:0:A3:A2:A1:A0; 9 V _{DD} 0101111
		I/O	
	NO.		
DGND	9	P	V _{EE}
SV _{EE} 2	10	P	3/4 PCB
V _{EE}	11	P	100V AGND
V _{DD}	12	P	RC V _{DD}
ENHP_CLS5	13	IL	CLS 5 V _{DD} PSE CLASS 5 PD CLASS 5 70W
AGND	14	P	
SENSE 4,3,2,1	15/18/21/24	I	SV _{EE} 0.25 MOS SENSE PCB
GATE 4,3,2,1	16/19/22/25	O	MOS
OUT 4,3,2,1	17/20/23/26	I/O	
N.C.	27		
SV _{EE} 1	28	P	1/2 PCB
EN_CL5	29	I	Class 5 DGND EN_CL5 DGND Class5 IEEE 802.3at EN_CL5 V _{DD} Class5 EN_CL5
AUTO	30	IL	/ DGND AUTO V _{DD} AUTO DGND AUTO
MIDSPAN	31	IL	DGND MIDSPAN V _{DD} DGND MIDSPAN
$\overline{\text{INT}}$	32	OD	DGND $\overline{\text{INT}}$ $\overline{\text{INT}}$ I ² C $\overline{\text{INT}}$ DGND
EP		P	V _{EE}

